J1000 U.S. PTO
10/003563
10/24/01

PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10003563	10/24/2001	~~710~~ 711	170	~~2188~~ 2187	MOAZZAMI

**APPLICANTS: Hemsing Craig; Hylands Dave; Jones Andrew; Li Henry; Pullman Susan;

N.M.

**CONTINUING DATA VERIFIED:
THIS APPLN CLAIMS BENEFIT OF 60/269,378 02/16/2001

N.M.

** FOREIGN APPLICATIONS VERIFIED:

BEST AVAILABLE COPY

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed	☐ yes ☒ no		ATTORNEY DOCKET NO
35 USC 119 conditions met	☐ yes ☒ no		13158US02
Verified and Acknowledged Examiners's intials	N.M.		

TITLE : Apparatus and method to reduce memory footprints in processor architectures

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED: Total Claims / Print Claim for O.G
ISSUE FEE: Amount Due / Date Paid	Primary Examiner	DRAWING: Sheets Drwg. / Figs.Drwg. / Print Fig.
☐ TERMINAL DISCLAIMER	PREPARED FOR ISSUE	Application Examiner

WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)